



2-27-04



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/644,603	08/20/2003	2811	1642	9319S-000540	11	23	11

27572

HARNES, DICKEY & PIERCE, P.L.C.  
P.O. BOX 828  
BLOOMFIELD HILLS, MI 48303

CONFIRMATION NO. 5576

UPDATED FILING RECEIPT



\*OC000000011879930\*

Date Mailed: 02/11/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Kazuyuki Yamada, Matsumoto-shi, JAPAN;  
Takeshi Ashida, Matsumoto-shi, JAPAN;  
Masahiko Nakazawa, Nagano-ken, JAPAN;  
Masanori Yumoto, Matsunoto-shi, JAPAN;

Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2002-240776 08/21/2002  
JAPAN 2003-197276 07/15/2003

If Required, Foreign Filing License Granted: 11/13/2003

Projected Publication Date: 05/20/2004

Non-Publication Request: No

Early Publication Request: No

## Title

Semiconductor device mounting method, semiconductor device mounting structure, ~~electro-~~

electro-

optical device, electro-optical device manufacturing method and electronic device

**Preliminary Class**

257

---

**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

**GRANTED**

The applicant has been granted a license under 35 U.S.C. 184, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" followed by a date appears on this form. Such licenses are issued in all applications where the conditions for issuance of a license have been met, regardless of whether or not a license may be required as set forth in 37 CFR 5.15. The scope and limitations of this license are set forth in 37 CFR 5.15(a) unless an earlier license has been issued under 37 CFR 5.15(b). The license is subject to revocation upon written notification. The date indicated is the effective date of the license, unless an earlier license of similar scope has been granted under 37 CFR 5.13 or 5.14.

This license is to be retained by the licensee and may be used at any time on or after the effective date thereof unless it is revoked. This license is automatically transferred to any related applications(s) filed under 37 CFR 1.53(d). This license is not retroactive.

The grant of a license does not in any way lessen the responsibility of a licensee for the security of the subject matter as imposed by any Government contract or the provisions of existing laws relating to espionage and the national security or the export of technical data. Licensees should apprise themselves of current regulations especially with respect to certain countries, of other agencies, particularly the Office of Defense Trade Controls, Department of State (with respect to Arms, Munitions and Implements of War (22 CFR 121-128)); the Office of Export Administration, Department of Commerce (15 CFR 370.10 (j)); the Office of Foreign Assets Control, Department of Treasury (31 CFR Parts 500+) and the Department of Energy.

**NOT GRANTED**

No license under 35 U.S.C. 184 has been granted at this time, if the phrase "IF REQUIRED, FOREIGN FILING LICENSE GRANTED" DOES NOT appear on this form. Applicant may still petition for a license under 37 CFR 5.12, if a license is desired before the expiration of 6 months from the filing date of the application. If 6 months has lapsed from the filing date of this application and the licensee has not received any indication of a secrecy order under 35 U.S.C. 181, the licensee may foreign file the application pursuant to 37 CFR 5.15(b).



Please type a plus sign (+) inside this box → ☐

# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	10/644,603
Filing Date	August 20, 2003
First Named Inventor	Kazuyuki Yamada
Group Art Unit	1642
Examiner Name	N/A
Attorney Docket Number	9319S-000540

Total Number of Pages in This Submission

## ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <b>Request for Corrected Official Filing Receipt and acknowledgement postcard.</b>
Remarks		The Commissioner is hereby authorized to charge any additional fees that may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 08-0750. A duplicate copy of this sheet is enclosed.

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

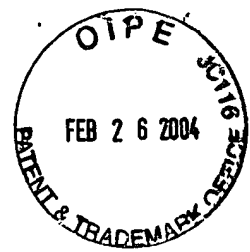
Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name	G. Gregory Schivley	Reg. No.	27,382
Signature					
Date	February 26, 2004				

## CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being deposited with the United States Postal Service as express mail in an envelope addressed to: Director of the U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450, or facsimile transmitted to the U.S. Patent and Trademark Office on the date indicated below.

Typed or printed name	G. Gregory Schivley	Express Mail Label No.	EV 406 074 603 US (2/26/2004)
Signature		Date	February 26, 2004

EV 406 074 603 US



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/644,603  
Filing Date: August 20, 2003  
Applicants: Kazuyuki Yamada, et al.  
Group Art Unit: 1642  
Examiner: N/A  
Title: SEMICONDUCTOR DEVICE MOUNTING METHOD,  
SEMICONDUCTOR DEVICE MOUNTING STRUCTURE,  
ELECTRO-OPTICAL DEVICE, ELECTRO-OPTICAL DEVICE  
MANUFACTURING METHOD AND ELECTRONIC DEVICE  
Attorney Docket: 9319S-000540

---

Director of the U.S. Patent and Trademark Office  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT**

Sir:

We acknowledge receipt of the Updated Official Filing Receipt for the above-identified patent application. However, the title is incorrect. Please correct your records to show **SEMICONDUCTOR DEVICE MOUNTING METHOD, SEMICONDUCTOR DEVICE MOUNTING STRUCTURE, ELECTRO-OPTICAL DEVICE, ELECTRO-OPTICAL DEVICE MANUFACTURING METHOD AND ELECTRONIC DEVICE** as the correct title. This correction can be seen in red on the copy enclosed. Thank you for your assistance.

Respectfully submitted,

Dated: February 26, 2004

By: 

G. Gregory Schwley  
Reg. No. 27,382

Harness, Dickey & Pierce, P.L.C.  
P.O. Box 828  
Bloomfield Hills, MI 48303  
GGS/msm